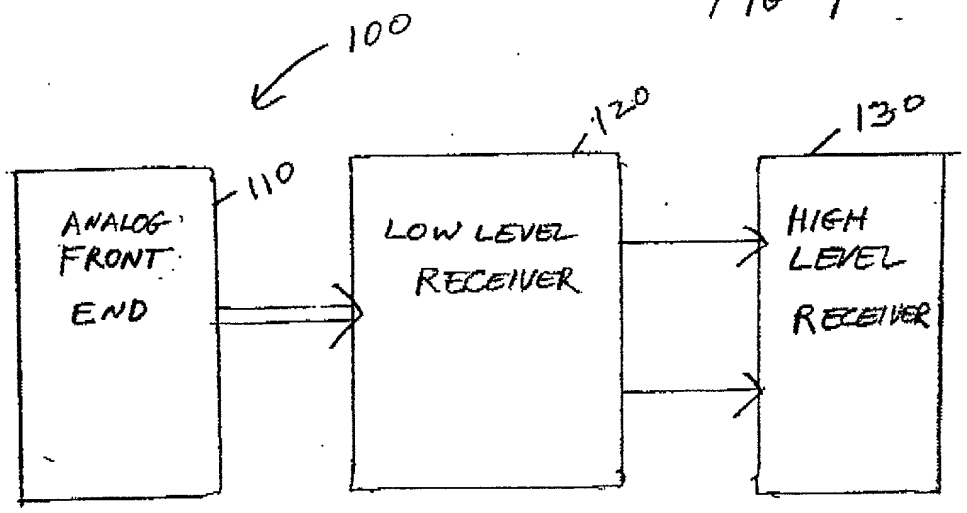
FIG 1


100
110
ANALOG FRONT END
120
LOW LEVEL RECEIVER
130
HIGH LEVEL RECEIVER

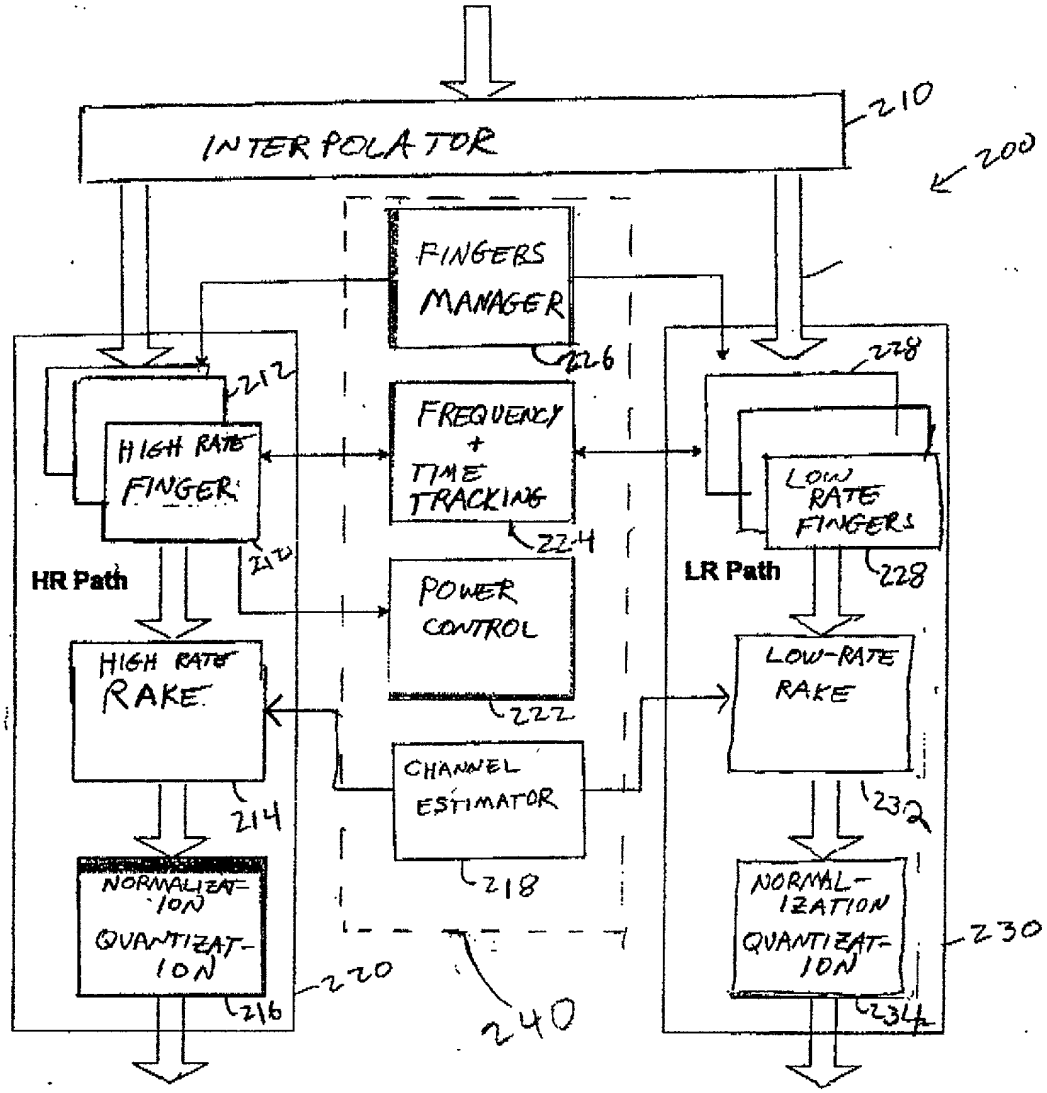

INTERPOLATOR
210
200
FINGERS MANAGER
226
FREQUENCY + TIME TRACKING
224
POWER CONTROL
222
CHANNEL ESTIMATOR
218
212
HIGH RATE FINGER
HR Path
214
HIGH RATE RAKE
NORMALIZATION QUANTIZATION
216
220
240
228
LOW RATE FINGERS
LR Path
228
LOW-RATE RAKE
232
NORMALIZATION QUANTIZATION
234
230

FIG 2

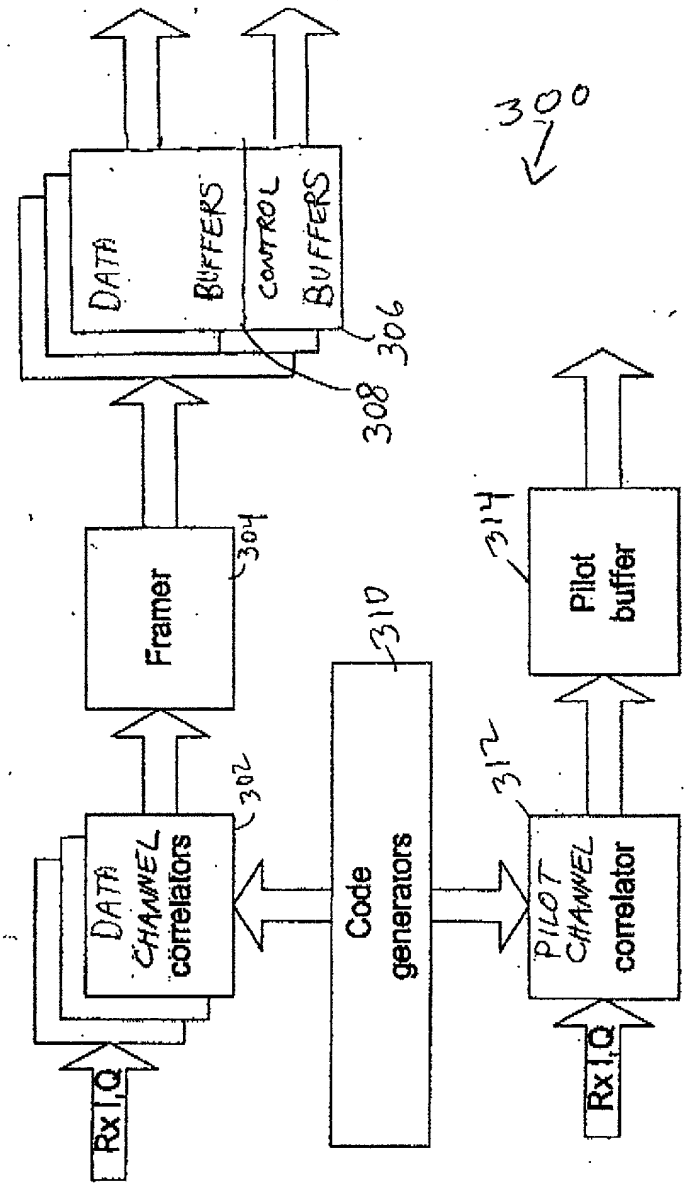

300
DATA BUFFERS
CONTROL BUFFERS
306
308
Framer
304
DATA CHANNEL correlators
302
Code generators
310
PILOT CHANNEL correlator
312
Pilot buffer
314
Rx I,Q
Rx I,Q

FIG 3

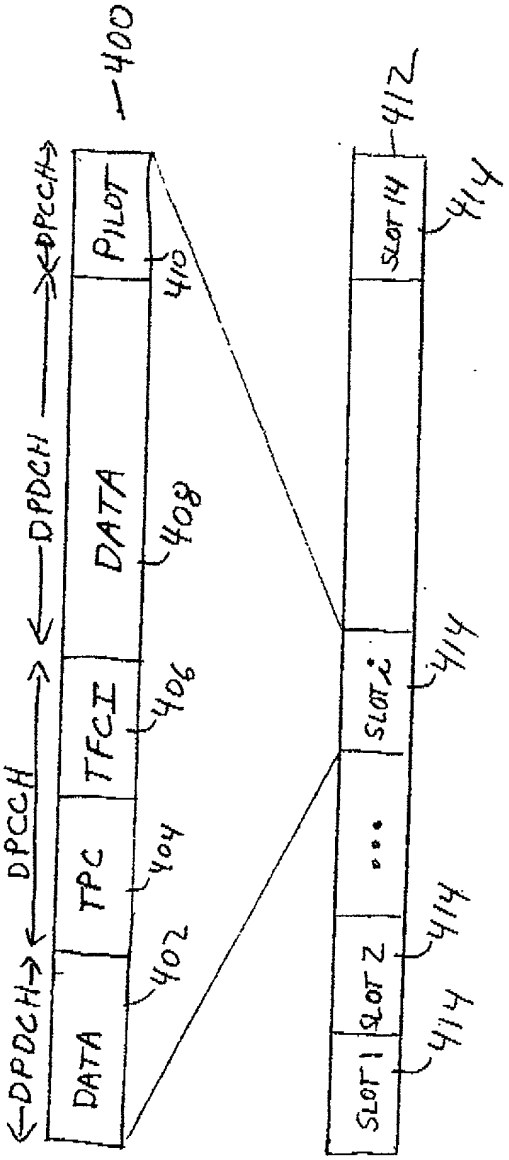
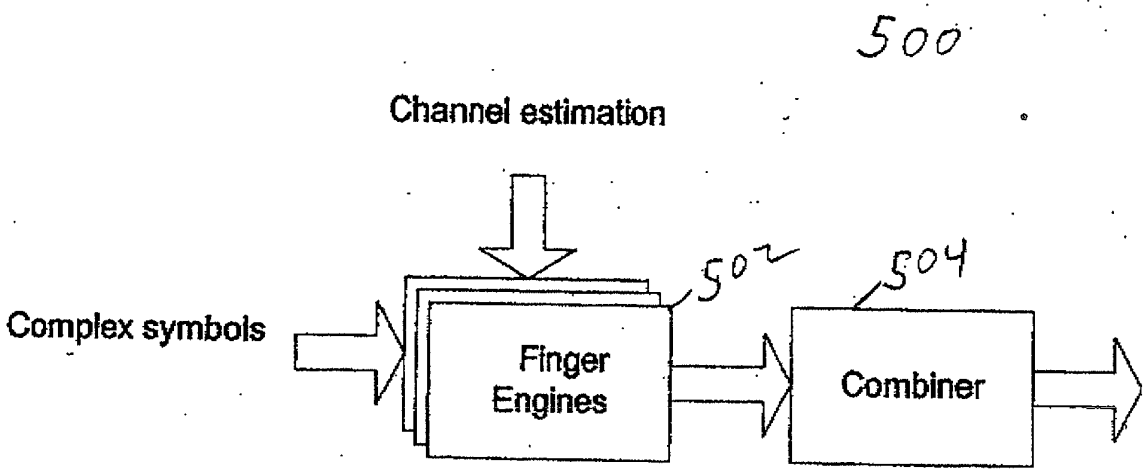

400
DPCCH
DPDCH
DPDCH
DPCCH
DPDCH
DATA
TPC
TFCI
DATA
PILOT
402
404
406
408
410
412
414
SLOT 0
SLOT 1
SLOT 2
SLOT 14

FIG 4

TO6T2T" 06052DOT



HIGH RATE RAKE FIG 5

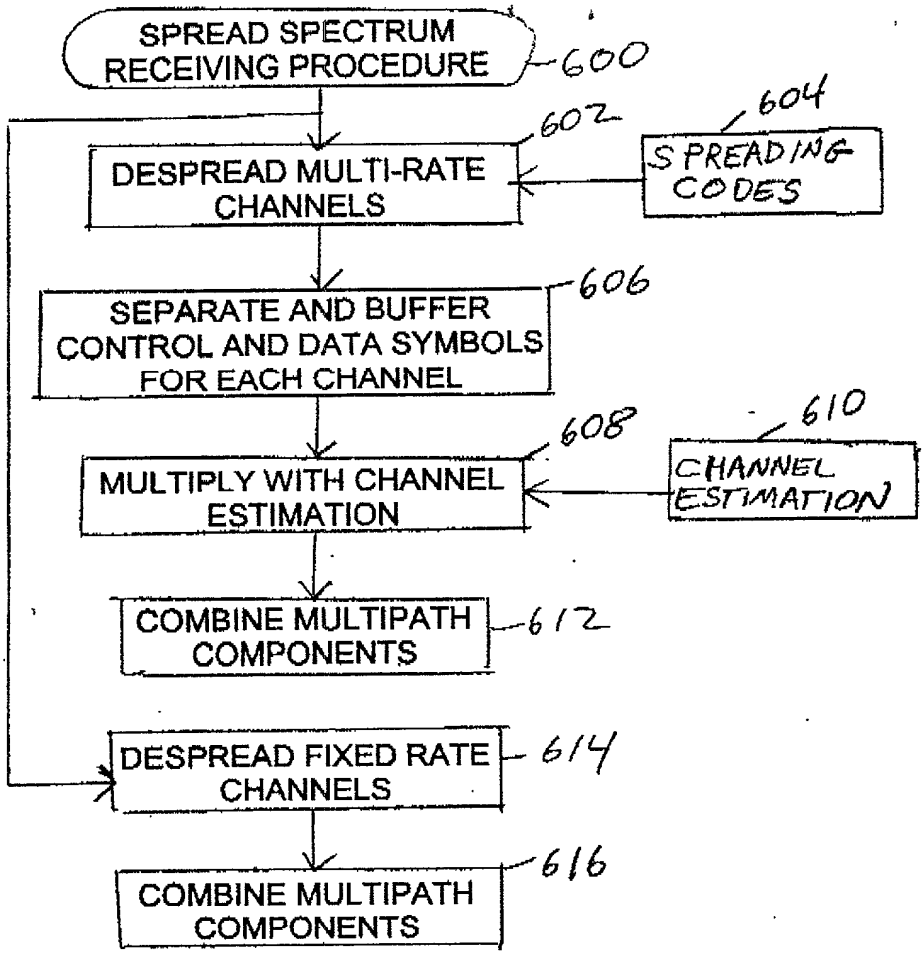

SPREAD SPECTRUM RECEIVING PROCEDURE
600
DESPREAD MULTI-RATE CHANNELS
602
SPREADING CODES
604
SEPARATE AND BUFFER CONTROL AND DATA SYMBOLS FOR EACH CHANNEL
606
MULTIPLY WITH CHANNEL ESTIMATION
608
CHANNEL ESTIMATION
610
COMBINE MULTIPATH COMPONENTS
612
DESPREAD FIXED RATE CHANNELS
614
COMBINE MULTIPATH COMPONENTS
616

FIG 6